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(54) Title: PHASE CONJUGATION CIRCUIT

(57) Abstract: A phase conjugate circuit is disclosed for deriving phase conjugation information from a main input signal of a given frequency. In one embodiment, the main input signal is mixed with the main output signal to provide the feedback signal and the reference signal is the PLL input signal. In an alternative embodiment, the reference signal is mixed with the main output signal to produce the feedback signal and the main input signal is the PLL input signal. In a further alternative embodiment, the main input signal is mixed with the reference signal to provide the PLL input signal and the main output signal is the feedback signal.



WO 2005/031977 A3